IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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mber 5, 2004 to for

Inventor(s):

Chyan

Case:

15-6-9

Serial No.:

09/648164

Filing Date:

8/25/2000

Examiner:

Dickey

Group Art Unit:

2826

United States Patent Office on Nove

or 703-872-9318 addressed to this Cor

Title:

ARCHITECTURE FOR CIRCUIT CONNECTION OF A VERTICAL

TRANSISTOR

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D. C. 20231

SIR:

PETITION FOR EXTENSION OF TIME

Applicant(s) petition(s) the Commissioner of Patents and Trademarks to extend the time for response to the action dated 5/05/04 for <u>3 (Three) months</u> from <u>8/05/04</u> to <u>11/05/04</u>.

11/16/2004 AJOHNSTEQUITED CONTEST the removed Duplicate copies of this petition are enclosed.

01 FC:1253

980.00 DA

Respectfully Submitted,

By:

Ferdinand M. Romano

Reg. 32,752

Attorney for Applicant

Date: 5 November 2004

PAGE 29 * RCVD AT 11/5/2004 2:05:38 PM [Eastern Standard Time] * SVR:USPTO-EFXRF-1/0 * DNIS:8729318 * CSID: * DURATION (mm-ss):02-14

Effective December 29, 1999										
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FORM PTO-875 (Rev. 12/99) Patent and Trademark Office, U.S. DEPARTMENT OF COMMERCE

Application or Docket Number